

Marios Kleanthous
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EDUCATION

- University of Cyprus, PhD in Computer Science in the field of Computer Architecture, April 2012.
- University of Cyprus, MSc in Computer Science, June 2006.
- National and Kapodistrian University of Athens, BSc in Informatics and Telecommunications, June 2004.

PROFESSIONAL PROFILE

- Experienced in teaching a wide variety of courses in the areas of Programming, Systems and Computer Architecture
- Familiar with the internal mechanisms, policies and procedures of academic institutions
- Experienced with European Research proposals and the management and completion of research grants
- Great team player with leadership qualities
- Comprehensive knowledge of computer architecture and programming principles
- Sound knowledge of software development cycle
- Profound understanding of large-scale systems' performance and cost parameters
- Ability to identify and resolve problems efficiently
- Ability to provide fast and integrated solutions to customization needs

PROGRAMMING AND SYSTEM SKILLS

- Programming languages: JAVA, C and C++
- Scripting languages/shells: Perl, bash and tcsh
- Web technologies: Servlets, HTML, PHP (pure PHP and Laravel framework), CSS and Javascript
- Web servers: Apache and Tomcat

- System administration: Linux (Suse, Debian/Ubuntu, RedHat/CentOS), Unix (Solaris), Mac (OS X), Windows (2003 and 2008 servers)
- Database Engines: MySQL, MariaDB, Microsoft SQL Server and PostgresSQL
- Virtualization: AWS, Azure, OpenVZ, KVM and ProxMox VE for management

WORK EXPERIENCE

- 09/2018 - present, Webmors, CEO.
- 02/2016 - 07/2019, Meritorius Audit Ltd, Senior Software Engineer Research Development.
- 07/2017 - 08/2018, MAP FinTech, Manager, Systems Architect.
- 02/2015 - 06/2017, MAP FinTech, Consultant, Senior Software Engineer.
- 09/2013 - 02/2015, University of Cyprus, Computer Architecture Lab, Researcher.
- 09/2013 - 01/2015, Mesoyios College, Computer Science Lecturer.
- 04/2012 - 08/2013, University of Cyprus, Computer Architecture Lab, Postdoctoral Researcher.
- 01/2007 - 03/2012, University of Cyprus, Computer Architecture Lab, Special Scientist.
- 09/2006 - 12/2006, ARM Ltd (United Kingdom, Cambridge), R&D Department, HiPEAC funded internship.
- 12/2004 - 08/2006, University of Cyprus, Computer Architecture Lab, Special Scientist.
- 12/2003 - 06/2004, Greek Ministry of Development, Collaborator of support and guidance "go-online".
- 04/2003 - 06/2004, National and Kapodistrian University of Athens, Energy Policy and Development Centre, Member of the Computer Scientist Support Team.

TEACHING EXPERIENCE

- 09/2014 - 01/2015, "COM102: Programming Principles I", Lecturer, Mesoyios College.
- 09/2014 - 01/2015, "COM100: Introduction to Computer Science", Lecturer, Mesoyios College.

- 02/2014 - 06/2014, "COM102: Fundamentals of Business Computing II", Lecturer, Mesoyios College.
- 09/2013 - 01/2014, "COM143: Business Computing", Lecturer, Mesoyios College.
- 09/2013 - 01/2014, "COM101: Fundamentals of Business Computing I", Lecturer, Mesoyios College.
- 01/2011 - 04/2011, "EPL372: Parallel Processing", Teaching assistant, University of Cyprus.
- 09/2010 - 12/2010, "EPL605: Advanced Computer Architecture", Teaching assistant, University of Cyprus.
- 01/2009 - 04/2009, "EPL372: Parallel Processing", Teaching assistant, University of Cyprus.
- 01/2009 - 04/2009, "EPL655: Advanced Computer Architecture II", Teaching assistant, University of Cyprus.
- 09/2008 - 12/2008, "EPL605: Advanced Computer Architecture", Teaching assistant, University of Cyprus.
- 09/2007 - 12/2007, "EPL421: Computer Architecture", Teaching assistant, University of Cyprus.
- 01/2007 - 04/2007, "EPL499: Special topics in computer science: Emerging Potentials and Limitations of modern Computing Systems", Teaching assistant, University of Cyprus.
- 09/2005 - 12/2005, "EPL421: Computer Architecture", Teaching assistant, University of Cyprus.

Committees

- Silicon Errors in Logic - System Effects (SELSE) 2012 - 2016 (Web Chair and Review Committee)
- International Symposium on Microarchitecture (MICRO47) 2014 (Web Chair)

Other

- Member of MENSA International, August 2014.
- Selected from HiPEAC for a company internship at ARM Ltd (United Kingdom, Cambridge) 09/2006 - 12/2006.

PUBLICATIONS

Refereed Journals

- "Comprehensive Characterization of an Open Source Document Search Engine", Zacharias Hadjilambrou, Marios Kleanthous, Georgia Antoniou,

Antoni Portero and Yiannakis Sazeides, Transactions on Architecture and Code Optimization (TACO), 2019

- "Toward Multi-Layer Holistic Evaluation of System Designs", Marios Kleanthous, Yiannakis Sazeides, Emre Ozer, Chrysostomos Nicopoulos, Panagiota Nikolaou and Zacharias Hadjilambrou, IEEE Computer Architecture Letters, 15(1): 58-61 (2016)
- "CATCH: A Mechanism for Dynamically Detecting Cache-Content-Duplication in Instruction Caches", Marios Kleanthous and Yiannakis Sazeides, ACM Transactions on Architecture and Code Optimization (TACO) 8.3 (2011): 11
- "Improving Branch Prediction by Considering Affectors and Affectees Correlations", Yiannakis Sazeides, Andreas Moustakas, Kipros Constantinides and Marios Kleanthous, Transactions on HiPEAC: 3(1):69-88, 2011

Refereed International Conference Proceedings

- "An Energy-Efficient and Error-Resilient Server Ecosystem Exceeding Conservative Scaling Limits", Georgios Karakonstantis, Konstantinos Tovletoglou, Lev Mukhanov, Hans Vandierendonck, Dimitrios Nikolopoulos, Peter Lawthers, Panos Koutsovasilis, Manolis Maroudas, Christos D. Antonopoulos, Christos Kalogirou, Nikolaos Bellas, Spyros Lalis, Sri Kumar Venugopal, Arnau Prat-Perez, Alejandro Lampropoulos, Marios Kleanthous, Andreas Diavastos, Zacharias Hadjilambrou, Panagiota Nikolaou, Yanos Sazeides, Pedro Trancoso, George Papadimitriou, Manolis Kaliorakis, Athanasios Chatzidimitriou, Dimitris Gizopoulos and Shidhartha Das, Design, Automation and Test in Europe (DATE 2018), Accepted for publication on November 2017
- "Characterization and Analysis of a Web Search Benchmark", Zacharias Hadjilambrou, Marios Kleanthous and Yiannakis Sazeides, IEEE International Symposium on Performance Analysis of Systems and Software, (ISPASS 2015), March 2015
- "Implicit Storing and Redundant Encoding of Attribute Information in Error Correction Codes", Yiannakis Sazeides, Emre Ozer, Danny Kershaw, Panagiota Nikolaou, Marios Kleanthous and Jaume Abella, The 46th Annual IEEE/ACM International Symposium on Microarchitecture, (MICRO 46), December 2013
- "An Analytical Framework for Estimating TCO and Exploring Data Center Design Space", Damien Hard, Marios Kleanthous, Isidoros Sideris, Ali Saidi, Emre Ozer and Yiannakis Sazeides, IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS 2013), April 2013
- "CATCH: A Mechanism for Dynamically Detecting Cache-Content-Duplication and its Application to Instruction Caches", Marios Kleanthous and Yiannakis Sazeides, Design, Automation and Test in Europe (DATE 2008), March 2008

- "The Significance of Affectors and Affectees Correlations for Branch Prediction", Yiannakis Sazeides, Andreas Moustakas, Kipros Constantinides and Marios Kleanthous, International Conference on High Performance Embedded Architectures and Compilers, January 2008

Patents

- "Entry Replacement Within a Data Store", Marios Kleanthous, Sami Yehia, Patent submitted January 2007.

Workshops

- "The Implications of Different DRAM Protection Techniques on Datacenter TCO", Panagiota Nikolaou, Yiannakis Sazeides, Marios Kleanthous and Lorena Ndreu, SELSE-11, April 2015.
- "Extrinsic and Intrinsic Text Cloning", Marios Kleanthous, Yiannakis Sazeides and Marios D. Dikaiakos, WIOSCA 2010 Workshop (held in conjunction with ISCA-37), June 2010.

Tutorials

- "COST-ET: An Estimation and Design Space Exploration Tool for Datacenters TCO", Organizers: Marios Kleanthous, Yiannakis Sazeides and Emre Ozer, ISCA 2013 Tutorial, June 2013.

Posters

- "Using Grid for Micro-Architecture Research", Liqiang He, Christiana Ioannou, Marios Kleanthous and Yiannakis Sazeides, EGEE 2007, October 2007
- "A Replacement Policy Based on Dynamic Profiling and Hashed Data", Marios Kleanthous, Sami Yehia, Yiannakis Sazeides and Emre Ozer, Poster presentation, ACACES 2007, July 2007.
- "Cache-Content-Duplication for Valid Blocks", Marios Kleanthous and Yiannakis Sazeides, Poster presentation, ACACES 2006, July 2006.
- "CATCH: A method for Dynamically Detecting Cache-Content-Duplication", Marios Kleanthous and Yiannakis Sazeides, Poster presentation, ACACES 2005, July 2005.

Technical Reports

- "Simulation Region Analysis for SPEC2000 Benchmarks", Marios Kleanthous and Yiannakis Sazeides, University of Cyprus, TR-CS-01-12, January 2012.
- "Dynamically Detecting Cache-Content-Duplication in Instruction Caches", Marios Kleanthous and Yiannakis Sazeides, University of Cyprus, TR-CS-03-2007, February 2007.
- "The Duplication of Content in Instruction Caches and its Performance Implications", Marios Kleanthous, Yiannakis Sazeides, University of Cyprus, TR-CS-01-2005, January 2005.

Thesis

- PhD Thesis: "Cache Content Duplication", April 2012.
- Master Thesis: "CATCH: A Method for Dynamically Detecting Cache-Content-Duplication and its Application to Instruction Caches", June 2006.
- Diploma Project: "A survey to Geographic Information Systems and implementation of a simple Geographic Information System", June 2004.